FIG. 1

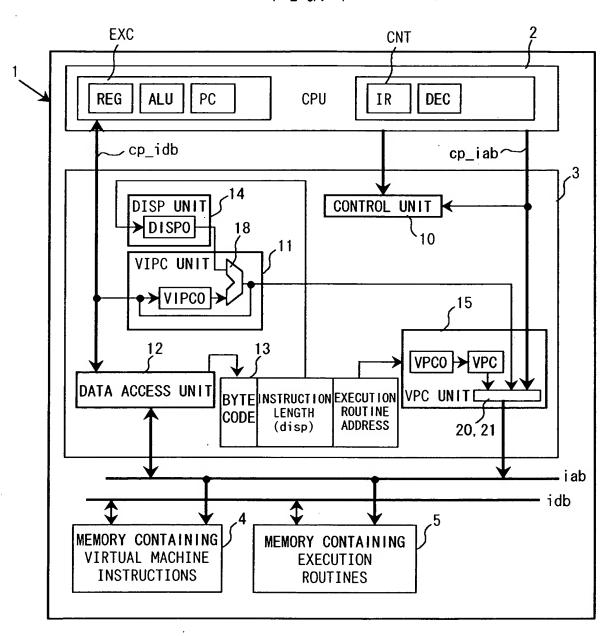


FIG. 2

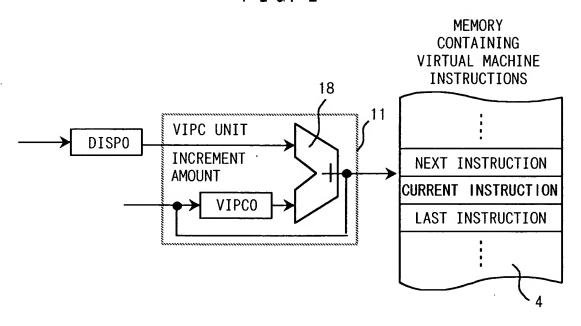


FIG. 3

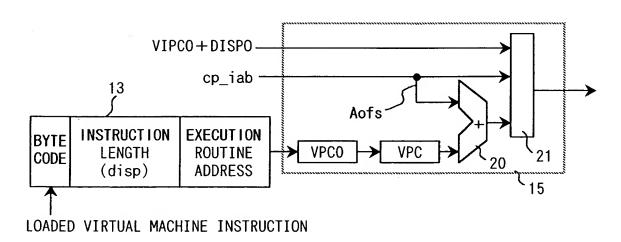


FIG. 4

;VIPCO Write ;PRG_TOP··· VIRTUAL MACHINE INSTRUCTION ADDRESS TO BE FIRST EXECUTED #H'PRG_TOP, @VIPCO ;PRG_TOP -> VIPC mov.w ;DISPO Write #H'00, @DISP0 ;H 0 -> DISP0 mov.w ; VPC0 UPDATE COMMAND ISSUANCE ; LOAD VIRTUAL MACHINE INSTRUCTION ON VIPCO + DISPO, EXECUTION ROUTINE ; ADDRESS AND INSTRUCTION LENGTH, AND UPDATE VPCO imes DISPO #H'00, @VPCOchg MOV.W ; UPDATE COMMAND ; JUMP TO HEAD OF VIRTUAL MACHINE INSTRUCTION EXECUTION SPACE (H' 00210000) #H'00210000, R1 mov.l jmp @R1 ; JUMP TO HEAD OF VIRTUAL MACHINE INSTRUCTION EXECUTION SPACE

FIG. 5

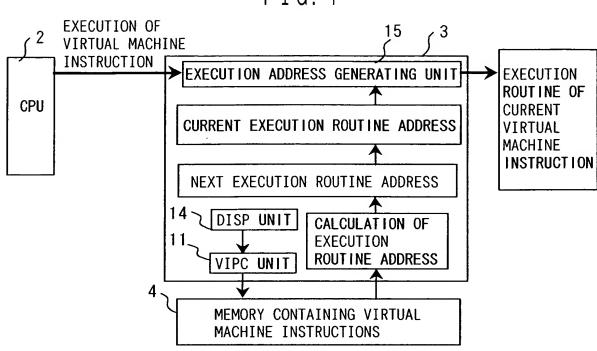
	OF EXECUT	TION ROUTINE (EXECUTING OPERATION [R1] -> [R2])
next:		; VIRTUAL MACHINE INSTRUCTION
_		EXECUTION SPACE ADDRESS
mov.l	@R1, @R	R2 ; EXECUTION PROCESSING PORTION
bra	next	; JUMP TO HEAD OF VIRTUAL MACHINE
		INSTRUCTION EXECUTION SPACE

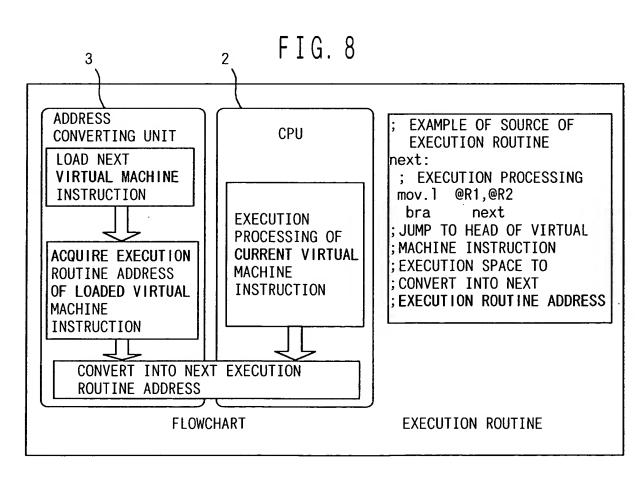
FIG. 6

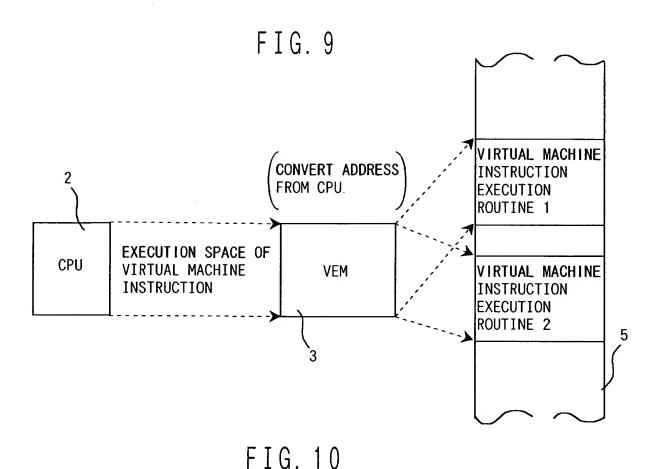
;EXECUTIO	N ROUTINE OF VARIA	ABLE-LENGTH INSTRUCTION AND BRANCH INSTRUCTION			
next:		; VIRTUAL MACHINE INSTRUCTION			
		EXECUTION SPACE ADDRESS			
•••••	••••••	; EXECUTION PROCESSING PORTION			

mov.w	R1, @DISPO	; INSTRUCTION LENGTH OF VARIABLE-LENGTH			
		; INSTRUCTION OR RELATIVE POSITION TO BRANCH			
		;DESTINATION FOR R1			
MOV.W	#00, @VPCOchg	; VPCO UPDATE COMMAND			
bra	next	;JUMP TO HEAD OF VIRTUAL MACHINE			
		INSTRUCTION EXECUTION SPACE			









4 0) 1

W. 16 1 ...

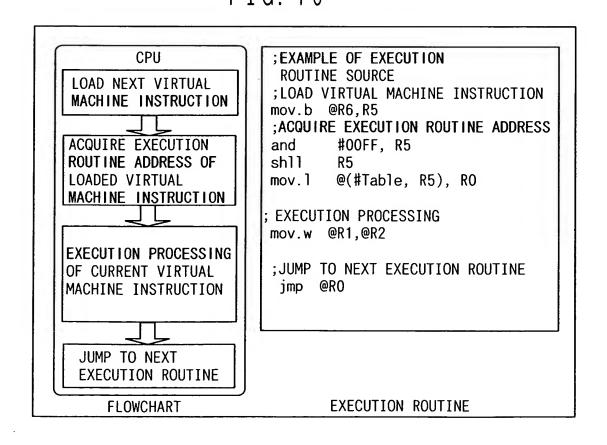


FIG. 11

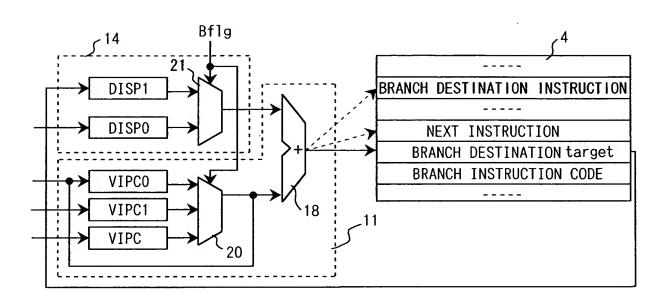
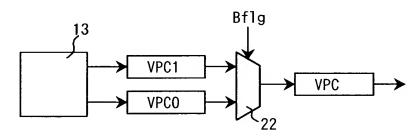


FIG. 12



next:

; VIRTUAL MACHINE INSTRUCTION

EXECUTION SPACE ADDRESS

mov.w @VIPCtoDISP1, R3

;[VIPC++] -> DISP1

mov.w #H'00, @VPC1chg

; VPC1 UPDATE COMMAND ; EXECUTION PROCESSING

bra next

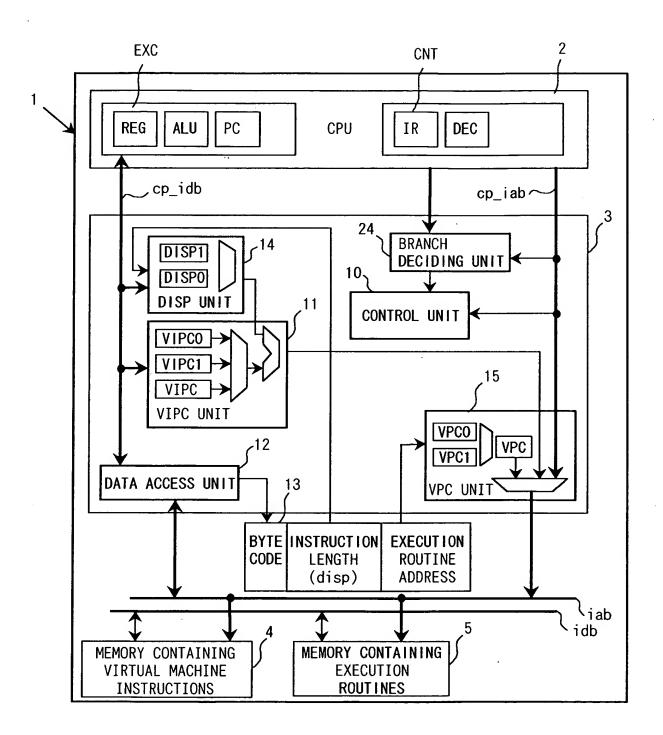
11.1.

; JUMP TO HEAD OF VIRTUAL MACHINE

INSTRUCTION EXECUTION SPACE

FIG. 14

:. '



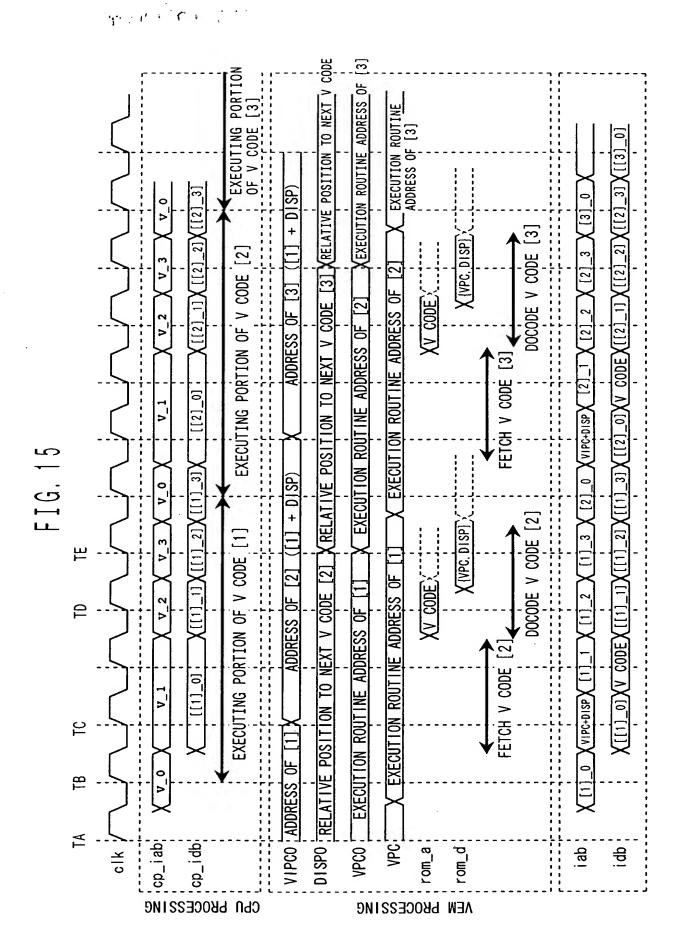
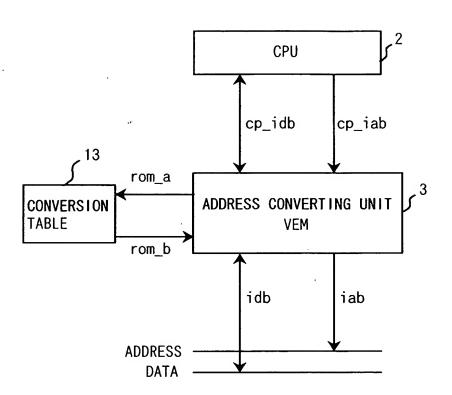


FIG. 16

THE STATE OF THE S



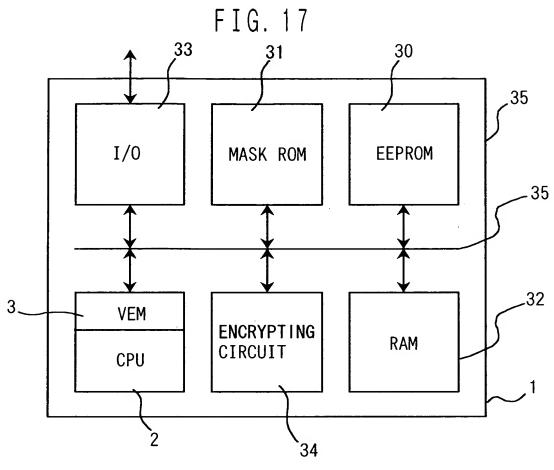


FIG. 18

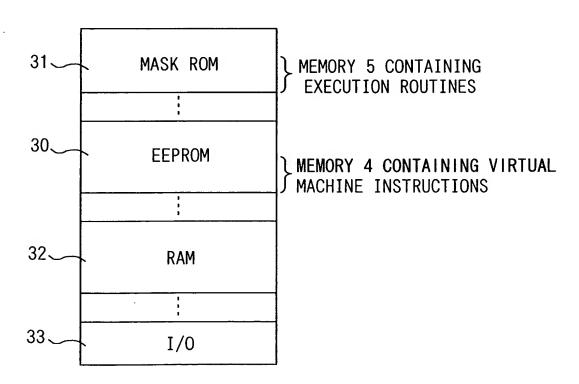


FIG. 19

1 1

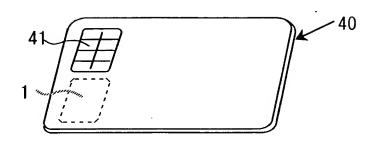


FIG. 20

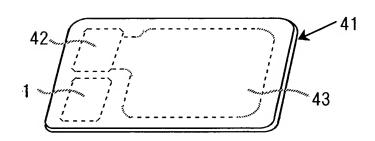


FIG. 21

